

# *IIDWay Technology Co. Ltd*

## **PRODUCT SPECIFICATIONS**

For Customer: \_\_\_\_\_

: APPROVAL FOR SPECIFICATION

Customer Model No. \_\_\_\_\_

: APPROVAL FOR SAMPLE

Module No.:   GZ70111-DIT021TH  

  Date : 2020-01-14  

### **Table of Contents**

No.	Item	Page
1	Cover Sheet(Table of Contents)	P1
2	Revision Record	P2
3	General Specifications	P3
4	Outline Drawing	P4
5	Absolute Maximum Ratings	P5
6	Electrical Specifications	P6-P9
7	Optical Characteristics	P10-P13
8	Reliability Test Items and Criteria	P14
9	Precautions for Use of LCD Modules	P15-P16

### **For Customer's Acceptance:**

Approved By	Comment

PREPARED	CHECKED	VERIFIED BY QA DEPT	VERIFIED BY R&D DEPT
YGM			



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## **3. General Specifications**

*GZ70111-DIT021TH is a TFT-LCD module. It is composed of a TFT-LCD panel, driver IC, FPC, a back light unit. The 2.1" display area contains 480X(RGB)x480 pixels and can display up to 262K colors. This product accords with ROHS environmental criterion.*

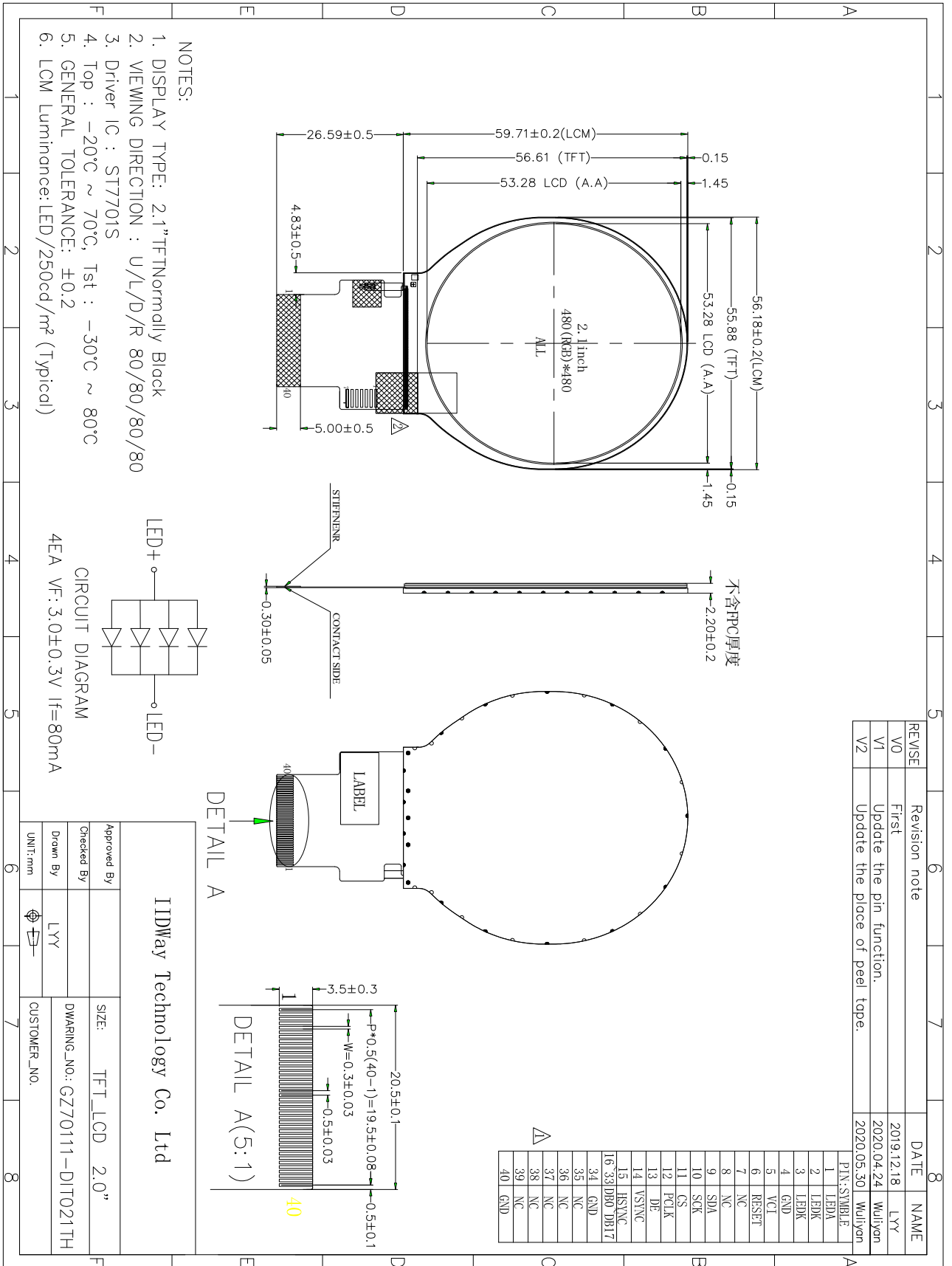
Item	Contents	Unit	Note
LCD Type	TFT	-	
Display color	262K	Color	1
Viewing Direction	ALL	O'Clock	
Operating temperature	-20~+70	°C	
Storage temperature	-30~+80	°C	
Module size	56.18X59.71X2.20	mm	2
Active Area(W×H)	53.28X53.28	mm	
Number of Dots	480×480	dots	
Controller	ST7701S	-	
Power Supply Voltage	2.8	V	
Backlight	4P-LEDs (white)	pcs	
Weight	---	g	
Interface	RGB18bit+SPI	-	

*Note 1: Color tune is slightly changed by temperature and driving voltage.*

*Note 2: Without FPC and Solder.*

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## 4.Outline.Drawing



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## 5. Absolute Maximum Ratings( $T_a=25^\circ\text{C}$ )

### 5.1 Electrical Absolute Maximum Ratings.( $V_{SS}=0\text{V}$ , $T_a=25^\circ\text{C}$ )

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V <sub>CI</sub>	-0.3	3.6	V	1,2

Notes:

1. If the module is above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.

2.  $V_{CI} > V_{SS}$  must be maintained.

3. Please be sure users are grounded when handing LCD Module.

### 5.2 Environmental Absolute Maximum Ratings.

Item	Storage		Operating		Note
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	-30°C	80°C	-20°C	70°C	1,2
Humidity	-	-	-	-	3

Notes:

1. The response time will become lower when operated at low temperature.

2. Background color changes slightly depending on ambient temperature.

The phenomenon is reversible.

3.  $T_a \leq 40^\circ\text{C}$ : 85%RH MAX.

$T_a > 40^\circ\text{C}$ : Absolute humidity must be lower than the humidity of 85%RH at 40°C.

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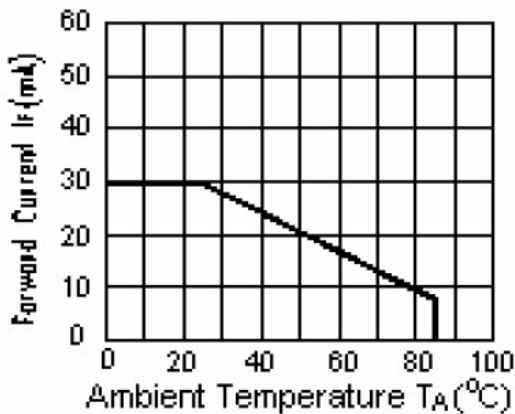
## 6. Electrical Specifications

### 6.1 Electrical characteristics ( $V_{SS}=0V, T_a=25^\circ C$ )

Parameter	Symbol	Condition	Min	Typ	Max	Unit	Note	
Power voltage	V <sub>CI</sub>	T <sub>a</sub> =25°C	2.5	2.8	3.6	V		
Input voltage	'H'	V <sub>IH</sub>	T <sub>a</sub> =25°C	0.7*V <sub>CI</sub>	-	V <sub>CI</sub>	V	
	'L'	V <sub>IL</sub>	T <sub>a</sub> =25°C	0	-	0.3*V <sub>CI</sub>	V	

### 6.2 LED backlight specification ( $V_{SS}=0V, T_a=25^\circ C$ )

Item	Symbol	Condition	Min	Typ	Max	Unit	Note
Supply voltage	V <sub>f</sub>	I <sub>f</sub> =80mA	2.7	3.0	3.3	V	
Uniformity	ΔBp	I <sub>f</sub> =80mA	75	80	-	%	
Life Time	time	I <sub>f</sub> =80mA	-	20K	-	hours	1



Note 1: Brightness to be decreased to 50% of the initial value at ambient temperature T<sub>A</sub>=25°C

### 6.3 Interface signals

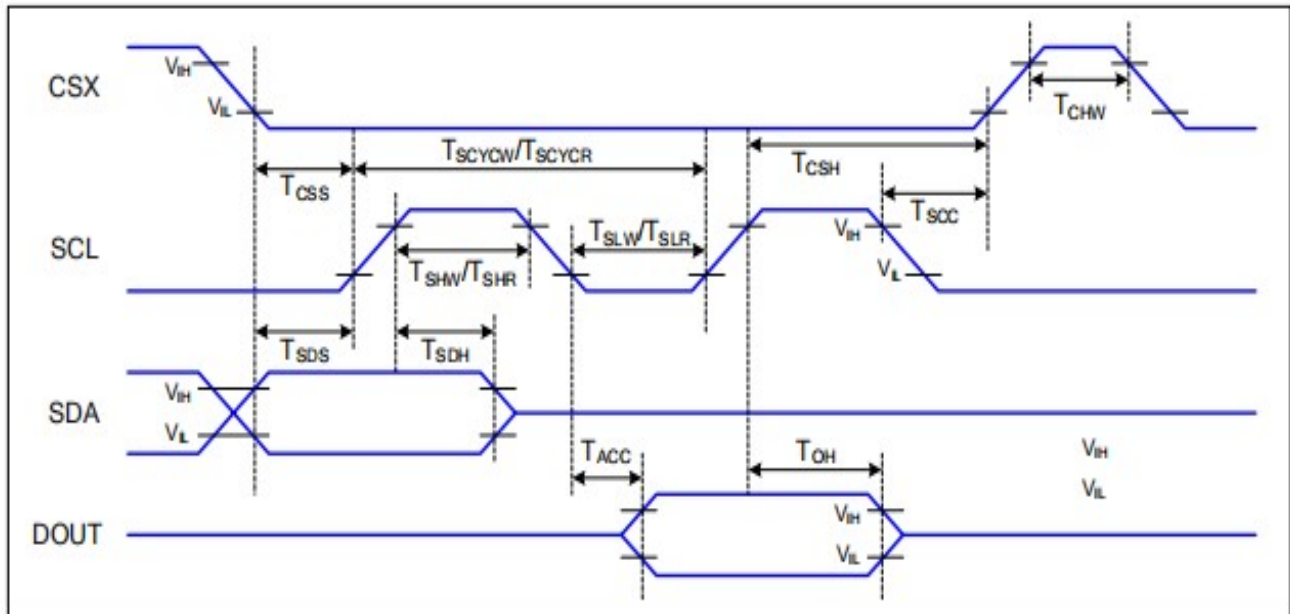
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Pin No.	Symbol	I/O	Function
1	LEDA	P	LED back light(Anode)
2-3	LEDK	P	LED back light(Cathode)
4	GND	P	Ground.
5	VCI	P	Power supply
6	RESET	I	Reset pin,active "L"
7-8	NC	-	No connection.
9	SDA	I	Serial Input data bus
10	SCK	I	Serial clock as serial interface.
11	CS	I	Chip select signal
12	PCLK	I	Data clock
13	DE	I	Data enable pin
14	VSYNC	I	Vertical sync input in RGB mode
15	HSYNC	I	Horizontal sync input in RGB mode
16-33	DB0-DB17	I	RGB data bus
34	GND	P	Ground.
35	TP-INT	I	CTP control pin,no use please NC
36	TP-SDA	I	
37	TP-SCL	I	
38	TP-RESET	I	
39	TP-VCI	I	
40	GND	P	Ground.

## **6.4 AC Characteristics**

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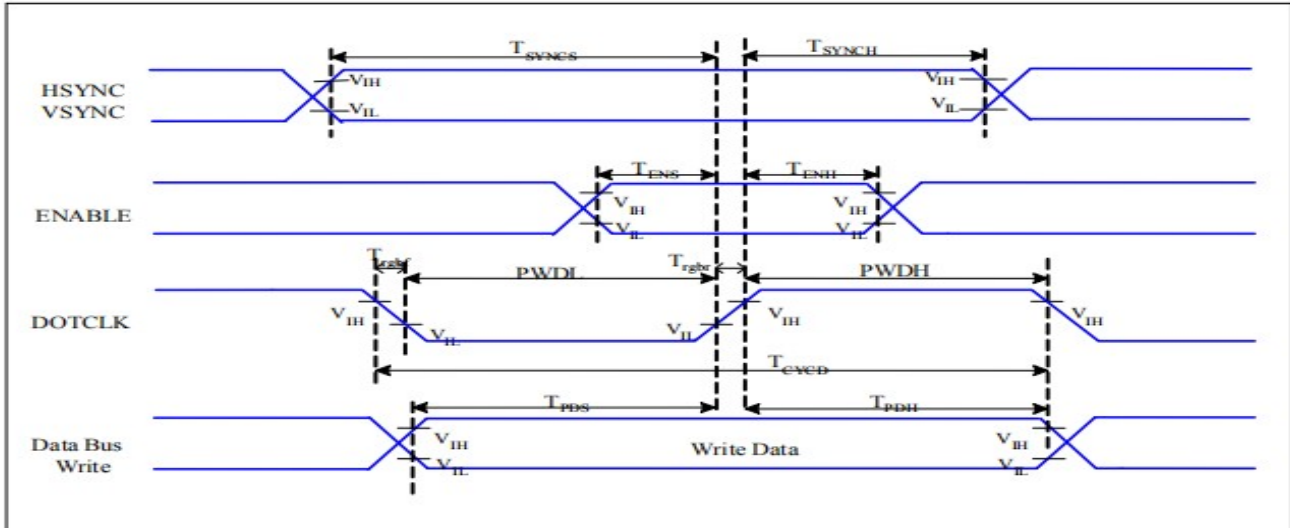
## Serial Interface Characteristics (3-line serial):



Signal	Symbol	Parameter	Min	Max	Unit	Description
CSX	$T_{CSS}$	Chip select setup time (write)	15		ns	
	$T_{CSH}$	Chip select hold time (write)	15		ns	
	$T_{CSS}$	Chip select setup time (read)	60		ns	
	$T_{SCH}$	Chip select hold time (read)	60		ns	
	$T_{CHW}$	Chip select "H" pulse width	40		ns	
SCL	$T_{SCYCW}$	Serial clock cycle (Write)	66		ns	
	$T_{SHW}$	SCL "H" pulse width (Write)	15		ns	
	$T_{SLW}$	SCL "L" pulse width (Write)	15		ns	
	$T_{SCYCR}$	Serial clock cycle (Read)	150		ns	
	$T_{SHR}$	SCL "H" pulse width (Read)	60		ns	
	$T_{SLR}$	SCL "L" pulse width (Read)	60		ns	
SDA	$T_{SDS}$	Data setup time	10		ns	
(DIN)	$T_{SDH}$	Data hold time	10		ns	

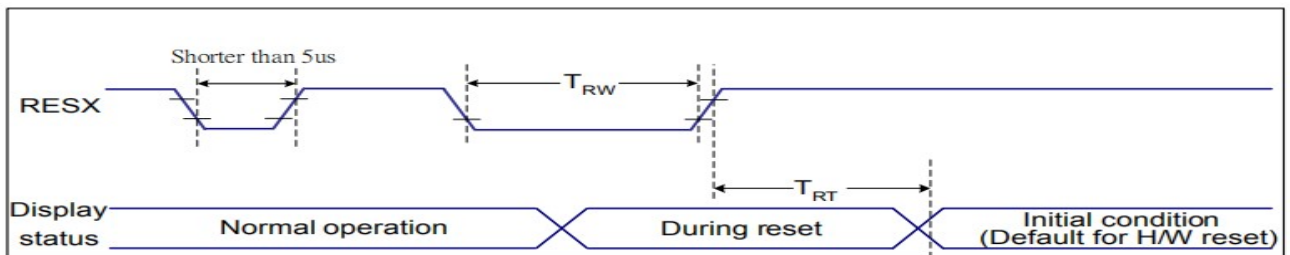
## RGB Interface Characteristics :

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Signal	Symbol	Parameter	MIN	MAX	Unit	Description
HSYNC, VSYNC	$T_{SYNCS}$	VSYNC, HSYNC Setup Time	5	-	ns	
ENABLE	$T_{ENS}$	Enable Setup Time	5	-	ns	
	$T_{ENH}$	Enable Hold Time	5	-	ns	
DOTCLK	PWDH	DOTCLK High-level Pulse Width	15	-	ns	
	PWDL	DOTCLK Low-level Pulse Width	15	-	ns	
	$T_{CYCD}$	DOTCLK Cycle Time	33	-	ns	
	Trghr, Trghf	DOTCLK Rise/Fall time	-	15	ns	
DB	$T_{PDS}$	PD Data Setup Time	5	-	ns	
	$T_{PDH}$	PD Data Hold Time	5	-	ns	

## 6.5 Reset timing



Related Pins	Symbol	Parameter	MIN	MAX	Unit
RESX	TRW	Reset pulse duration	10	-	us
	TRT	Reset cancel	-	5 (Note 1, 5)	ms
-			120 (Note 1, 6, 7)	ms	

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## 7. Optical Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Brightness	Bp	$\theta=0^\circ$ $\Phi=0^\circ$	200	250	-	Cd/m <sup>2</sup>	1
Uniformity	$\Delta$ Bp		75	80	-	%	1,2
Viewing Angle	3:00	Cr $\geq$ 10	-	80	-	Deg	3
	6:00		-	80	-		
	9:00		-	80	-		
	12:00		-	80	-		
Contrast Ratio	Cr	$\theta=0^\circ$ $\Phi=0^\circ$	800	1000	-	-	4
Response Time	T <sub>r</sub> +T <sub>f</sub>		-	30	35	ms	5
Color of CIE Coordinate	W	x	Typ. -0.05	0.301	Typ. +0.05	-	1,6
		y		0.333		-	
	R	x		0.655		-	
		y		0.327		-	
	G	x		0.284		-	
		y		0.594		-	
	B	x		0.138		-	
		y		0.111		-	
NTSC Ratio	S	60	69	-	%		

*Note: The parameter is slightly changed by temperature, driving voltage and materiel*

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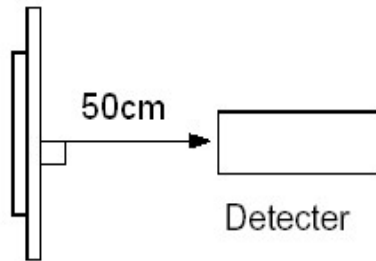
Note 1: The data are measured after LEDs are turned on for 5 minutes. LCM displays full white.

The brightness is the average value of 9 measured spots. Measurement equipment BM-7 (Φ5mm)

Measuring condition:

- Measuring surroundings: Dark room.
- Measuring temperature:  $T_a=25^{\circ}\text{C}$ .
- Adjust operating voltage to get optimum contrast at the center of the display.

Measured value at the center point of LCD panel after more than 5 minutes while backlight turning on.

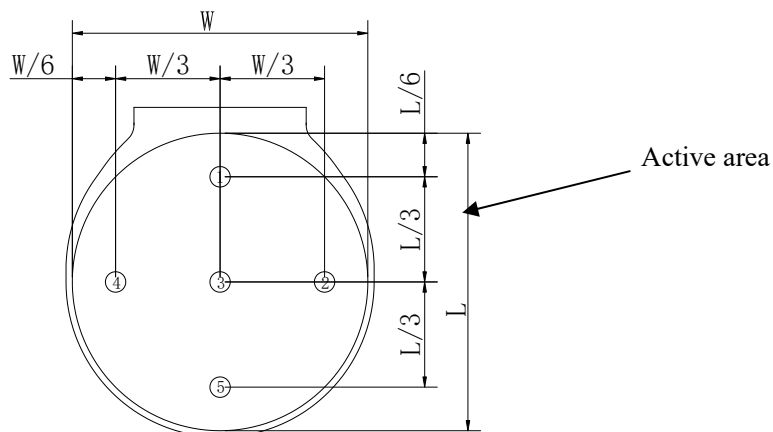


Note 2: The luminance uniformity is calculated by using following formula.

$$\Delta B_p = B_p (\text{Min.}) / B_p (\text{Max.}) \times 100 (\%)$$

$B_p (\text{Max.})$  = Maximum brightness in 9 measured spots

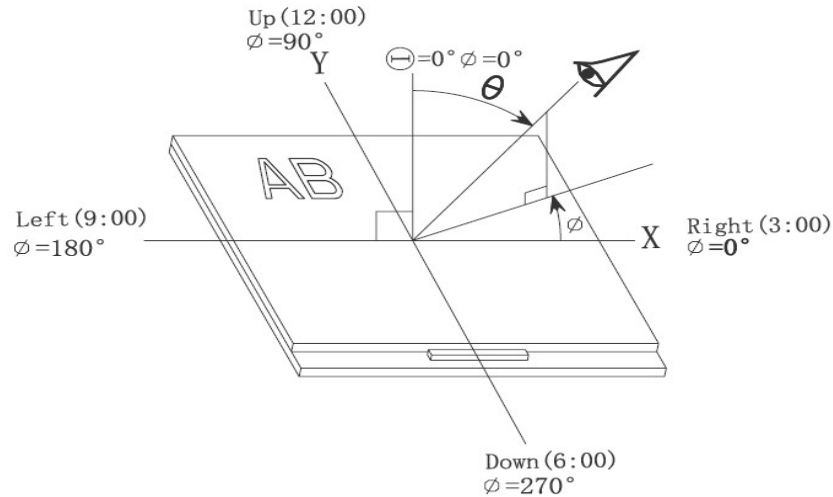
$B_p (\text{Min.})$  = Minimum brightness in 9 measured spots.



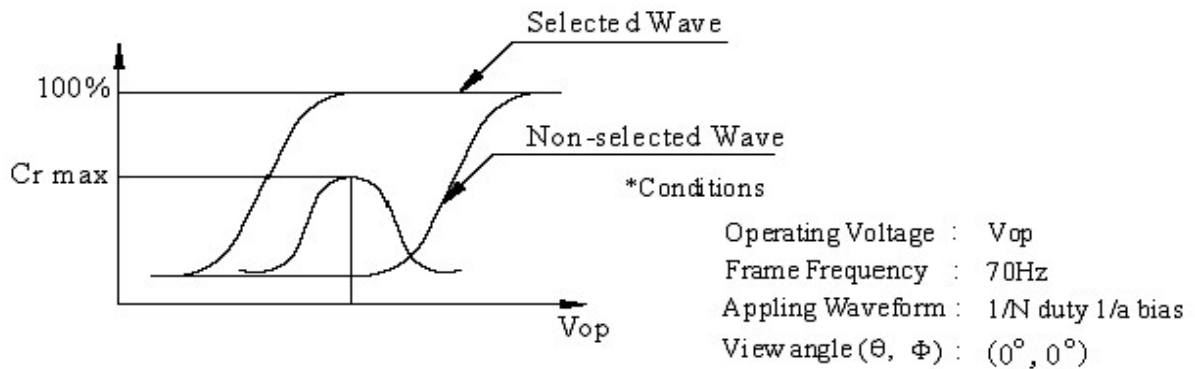
Note 3: The definition of viewing angle:

Refer to the graph below marked by  $\theta$  and  $\Phi$

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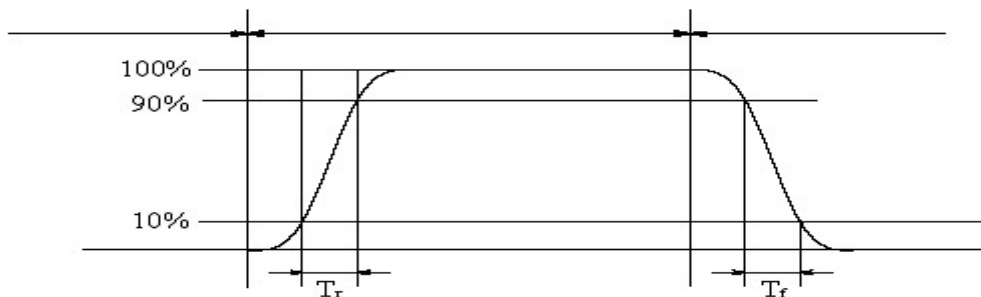
Note 4: Definition of contrast ratio.( Test LCD using DMS501)



$$\text{Contrast ratio}(Cr) = \frac{\text{Brightness of selected dots}}{\text{Brightness of non-selected dots}}$$

Note 5: Definition of Response time. (Test LCD using DMS501):

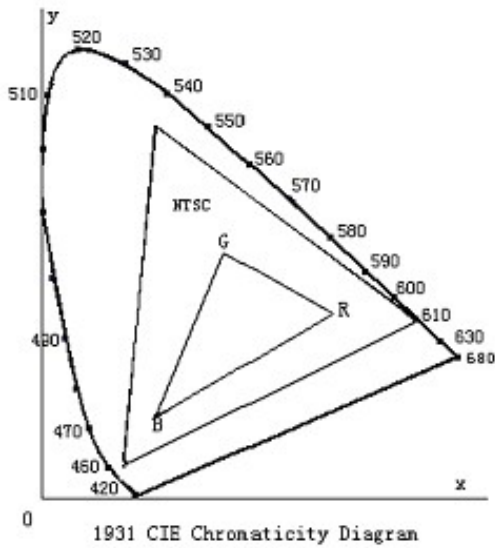
The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



The definition of response time

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Note 6: Definition of Color of CIE Coordinate and NTSC Ratio.

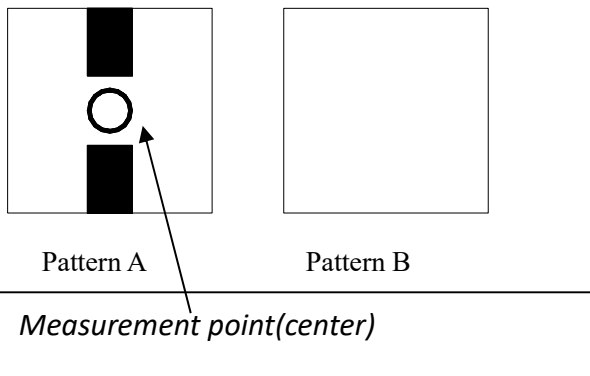


Color gamut:

$$S = \frac{\text{area of RGB triangle}}{\text{area of NTSC triangle}} \times 100\%$$

Note 7: Definition of cross talk.

Cross talk ratio(%) =  $\frac{|\text{pattern A Brightness} - \text{pattern B Brightness}|}{\text{pattern A Brightness}} \times 100$



Electric volume value =  $3F \pm 3Hex$

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## 8. Reliability Test Items and Criteria

Test Item	Test condition	Remark
High Temperature Storage	Ta = 80°C 96hrs	Note1,Note3, 4
Low Temperature Storage	Ta = -30°C 96hrs	Note1,Note3, 4
High Temperature Operation	Ts = 70°C 96hrs	Note2,Note3, 4
Low Temperature Operation	Ta = -20°C 96hrs	Note1,Note3, 4
Operation at High Temperature/Humidity	+60°C, 90%RH 96hrs	Note3, 4
Thermal Shock	-30°C/30 min ~ +80°C/30 min for a total 10 cycles, Start with cold temperature and end with high temperature.	Note3, 4
Vibration Test	Frequency range:10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X. Y. Z. (6 hours for total)	
Mechanical Shock	100G 6ms,±X, ±Y, ±Z 3 times for each direction	
Package Vibration Test	Random Vibration : 0.015G*G/Hz from 5-200HZ, -6dB/Octave from 200-500HZ 2 hours for each direction of X. Y. Z. (6 hours for total)	
Package Drop Test	Height:60cm 1 corner, 3 edges, 6 surfaces	
Electro Static Discharge	±2KV, Human Body Mode, 100pF/1500Ω	

Note 1: Ta is the ambient temperature of samples.

Note 2: Ts is the temperature of panel's surface.

Note 3: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.

Note 4: Before cosmetic and function test, the product must have enough recovery time,at least 2 hours at room temperature

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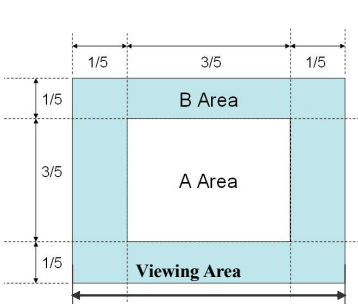
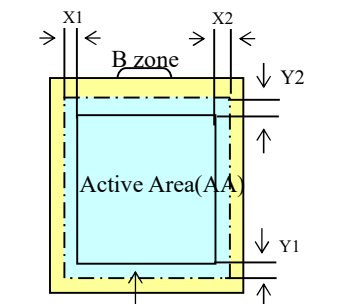
## 9 Quality level

### 9.1 Classification of defects

**Major defects (MA):** A major defect refers to a defect that may substantially degrade usability for product applications, including all functional defects (such as no display, abnormal display, open or missing segment, short circuit, missing component), outline dimension beyond the drawing, progressive defects and those affecting reliability.

**Minor defects (MI):** A minor defect refers to a defect which is not considered to be able to substantially degrade the product application or a defect that deviates from existing standards almost unrelated to the effective use of the product or its operation, such as black spot, white spot, bright spot, pinhole, black line, white line, contrast variation, glass defect, polarizer defect, etc.

### 9.2 Definition of inspection range

<p>For dot defect of TFT LCD which is not smaller than 3 inches, dividing three areas to make a judgment (according to figure 1).</p> <p>A area : center of viewing area          B area : periphery of viewing area          C area : Outside viewing area</p> <p>For other defects, dividing two areas to make a judgment (according figure 2).</p> <p>A zone : Inside Viewing area          B zone : Outside Viewing area</p> <p>X1(A.A~V.A): 2mm    X2(A.A~V.A): 2mm          Y1(A.A~V.A): 2mm    Y2(A.A~V.A): 2mm</p>	 <p style="text-align: center;">Figure 1</p>  <p style="text-align: center;">Figure 2</p>
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### 9.3 Inspection items and general notes

<p>General notes</p>	<p>Should any defects which are not specified in this standard happen, additional standard shall be determined by mutual agreement between customer and us.</p> <p>Viewing area should be the area which us guarantees.</p> <p>Limit sample should be prior to this Inspection standard.</p> <p>Viewing judgment should be under static pattern.</p> <p>Inspection conditions</p> <p>Inspection distance: 250 mm (from the sample)      Temperature : 25±5 °C</p> <p>Inspection angle : 45 degrees in 6 o'clock direction (all defects in viewing area should be inspected from this direction)</p>	
<p>Inspection items</p>	<p>Pinhole, Bright spot, Black spot, White spot, Black line, White Line, Foreign particle, Bubble</p>	<p>The color of a small area is different from the remainder.          The phenomenon doesn't change with voltage</p>
	<p>Contrast variation</p>	<p>The color of a small area is different from the remainder.          The phenomenon changes with voltage</p>
	<p>Polarizer defect</p>	<p>Scratch, Dirt, Particle, Bubble on polarizer or between polarizer and glass</p>

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	Dot defect (TFT LCD)	The pixel appears bright or dark abnormally when display
	Functional defect	No display, Abnormal display, Open or missing segment, Short circuit, False viewing direction
	Glass defect	Glass crack, Shaved corner of glass, Surplus glass
	PCB defect	Components assembly defect

## 9.4 Outgoing Inspection level

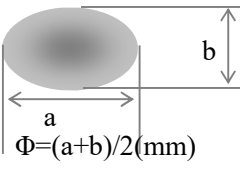
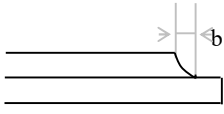
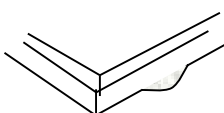
Outgoing Inspection standard	Inspection conditions	Inspection				
		Min.	Max.	Unit	IL	AQL
Major Defects	See 9.3 general notes	See 9.5			II	0.65
Minor Defects	See 9.3 general notes	See 9.5			II	1.0

Note: Sampling standard conforms to GB2828

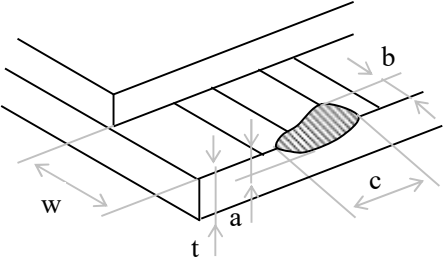
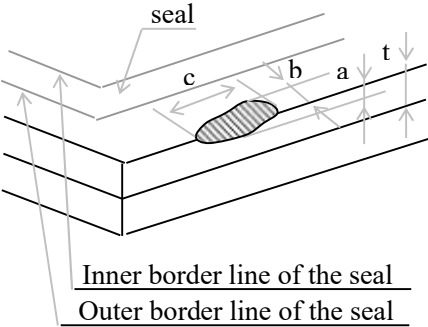
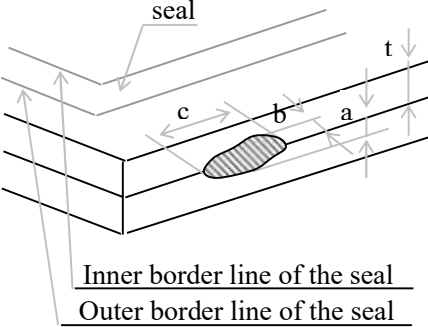
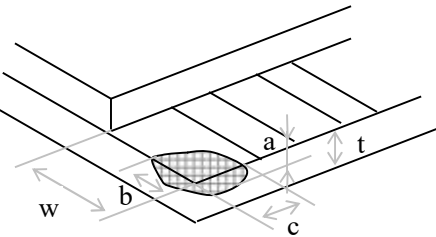
## 9.5 Inspection Items and Criteria

Inspection items			Judgment standard					
			Category		Acceptable number			
					A zone	B zone		
1	Black spot, White spot, Bright spot, Pinhole, Foreign Particle, Particle in or on glass, Scratch on glass		A	$\Phi \leq 0.10$	Neglected			
			B	$0.10 < \Phi \leq 0.25$	2			
			C	$0.25 < \Phi$	0			
			D	-	-			
			Total defective point(B,C)		1		Neglected	
2	Black line, White line, and Particle Between Polarizer and glass, Scratch on glass		A	$W \leq 0.03$	Neglected			
			B	$0.03 < W \leq 0.05$ $L \leq 3.0$	1			
			C	$0.05 < W \leq 0.1$ $L > 3.0$	0			
			Total defective point(B,C)		1			

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3	Contrast variation	 <p style="text-align: center;"><math>\Phi = \frac{a+b}{2}(\text{mm})</math></p>	A	$\Phi < 0.25$	Neglected	Neglected
			B	$0.25 < \Phi \leq 0.3$	2	
			C	$0.3 < \Phi \leq 0.4$	1	
			D	$0.4 < \Phi$	0	
			Total defective point(B,C)			
4	Bubble inside cell		any size		none	none
5	Polarizer defect (if Polarizer is used)	Scratch ,damage on polarizer, Particle on polarizer or between polarizer and glass. Bubble, dent and convex	Refer to item 1 and item 2.			
			A	$\Phi \leq 0.3$	Neglected	Neglected
			B	$0.3 < \Phi \leq 0.4$	1	
			C	$0.4 < \Phi$	0	
6	Surplus glass	Stage surplus glass 	$B \leq 0.3\text{mm}$			
		Surrounding surplus glass 	Should not influence outline dimension and assembling.			
7	Open segment or open common	Not permitted				
8	Short circuit	Not permitted				
9	False viewing direction	Not permitted				
10	Contrast ratio uneven	According to the limit specimen				
11	Crosstalk	According to the limit specimen				
12	Black /White spot(display)	Refer to item 1				
13	Black /White line(display)	Refer to item 2				

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Inspection items		Judgment standard		Acceptable number
		Category(application: B zone)		
14	Glass defect crack	i ) The front of lead terminals 	A $a \leq t, b \leq 1/5W, c \leq 3\text{mm}$	Max.3 defects allowed
		ii ) Surrounding crack-non-contact side 	B Crack at two sides of lead terminals should not cover patterns and alignment mark  $b < \text{Inner borderline of the seal}$	
		iii ) Surrounding crack- contact side 	$b < \text{Outer borderline of the seal}$	
		iv ) Corner 	A $a \leq t, b \leq 3.0, c \leq 3.0$  B Glass crack should not cover patterns u and alignment mark and patterns.	

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Inspection items		Judgment standard	
		Category(application: B zone)	
15	PCB defect	<p>Component soldering:                      No cold soldering, short, open circuit, burr, tin ball                      The flat encapsulation component position deviation must be less than 1/3 width of the pin (Pic.1);                      the sheet component deviation:                      Pin deviates from the pad and contact with the near components is not permitted (Pic.2)</p>	<p>The top diagram shows a component with leads on a pad. The distance between the leads is labeled <math>L \leq W/2</math> and the width of the pad is labeled <math>W</math>. The bottom diagram shows a component with leads on a pad. The distance between the leads is labeled <math>L2 &gt; 0</math> and the distance from the lead to the pad edge is labeled <math>L1 &gt; 0</math>.</p>
		<p>lead defect:                      The lead lack must be less than 1/3 of its width;                      The lead burr must be less than 1/3 of the seam;                      Impurities connect with the near leads is not permitted</p>	
		<p>Connector soldering:                      Soldering tin is at contact position of the plug and socket is not permitted                      No foundation is scald                      Serious cave distortion on plug and socket contact pin is not permitted</p>	<p>The top diagram shows a plug and socket with soldering tin. The labels are 'head' and 'Base Board'. The text below the diagram says 'Soldering tin is not permit in this area'. The bottom diagram shows a plug and socket with soldering tin. The labels are 'socket' and 'Base Board'. The text below the diagram says 'Soldering tin is not permit in this area'.</p>
<p>Glue on root of the speaker receiver and motor lead:                      The insulative coat of the lead must join into the PCB; the protected glue must envelop to the insulative coat.</p>	<p>The diagram shows a lead on a PCB. The labels are 'Glue', 'Lead', 'PCB', and 'Insulative coat'.</p>		

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## **10. Precautions for Use of LCD Modules**

### **10.1 Handling Precautions**

10.1.1 *The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.*

10.1.2 *If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.*

10.1.3 *Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.*

10.1.4 *The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.*

10.1.5 *If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:*

— *Isopropyl alcohol*      — *Ethyl alcohol*

*Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:*

— *Water*                      — *Ketone*                      — *Aromatic solvents*

10.1.6 *Do not attempt to disassemble the LCD Module.*

10.1.7 *If the logic circuit power is off, do not apply the input signals.*

10.1.8 *To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.*

*a. Be sure to ground the body when handling the LCD Modules.*

*b. Tools required for assembly, such as soldering irons, must be properly ground.*

*c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.*

*d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.*

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## **10.2 Storage precautions**

10.2.1 *When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.*

10.2.2 *The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:*

*Temperature :           0°C ~ 40°C*

*Relatively humidity: ≤80%*

10.2.3 *The LCD modules should be stored in the room without acid, alkali and harmful gas.*

**10.3 *The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.***

**END**

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