

IIDWay Technology Co. Ltd

PRODUCT SPECIFICATIONS

For Customer: _____

: APPROVAL FOR SPECIFICATION

Customer Model No. _____

: APPROVAL FOR SAMPLE

Module No.: GZ70136-DTT101WS

 Date : 2018-07-27

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For Customer's Acceptance:

Approved By	Comment

PREPARED	CHECKED	VERIFIED BY QA DEPT	VERIFIED BY R&D DEPT

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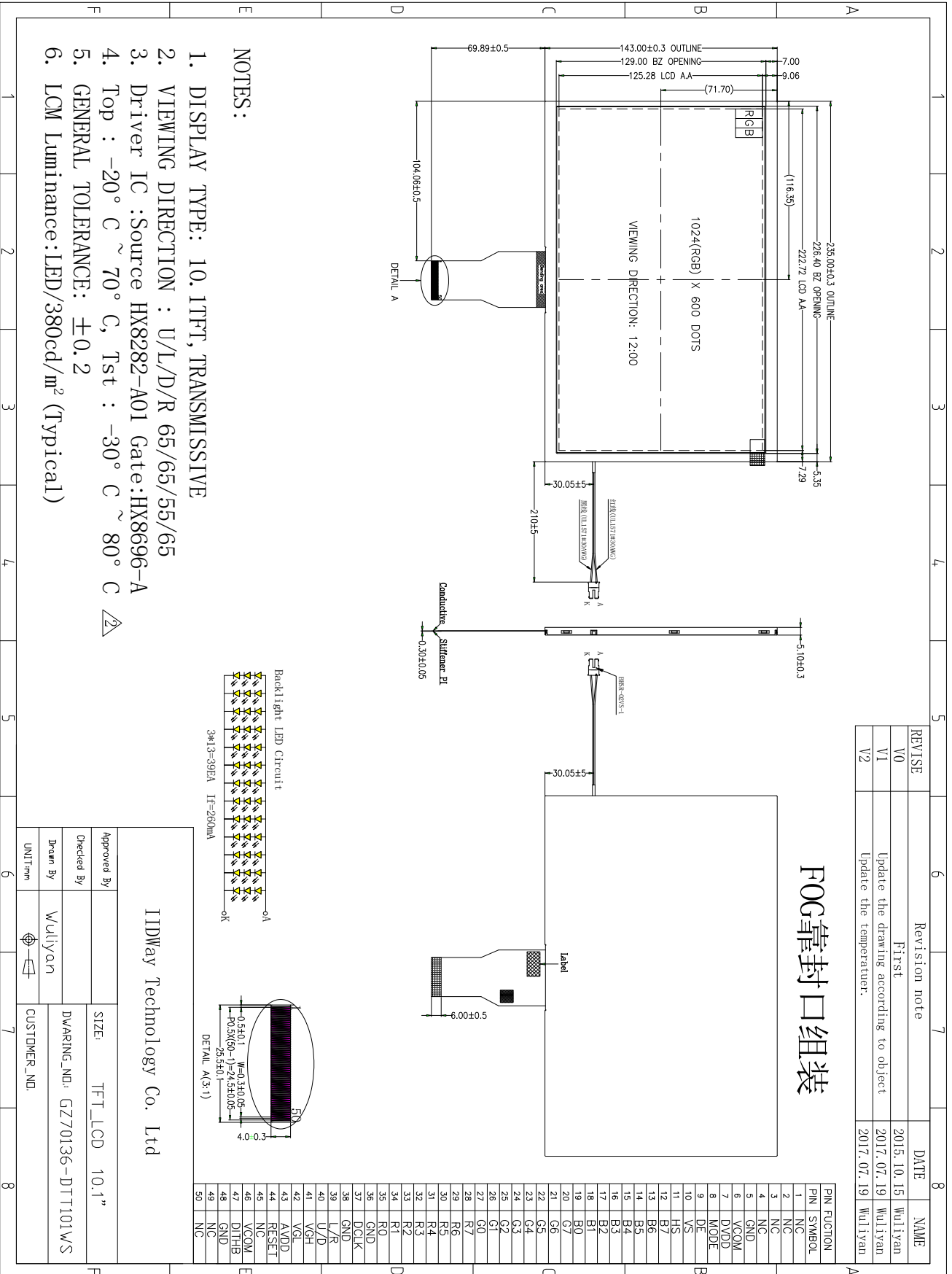
3. General Specifications

GZ70136-DTT101WS is a TFT-LCD module. It is composed of a TFT-LCD panel, driver IC, FPC, a back light unit. The 10.1" display area contains 1024x600 pixels and can display up to 16.7M colors. This product accords with RoHS environmental criterion.

Item	Contents	Unit	Note
LCD Type	TFT	-	
Display color	16.7M	-	
Viewing Direction	12	O'Clock	
Gray scale inversion direction	6	O'Clock	
Operating temperature	-0~+50	°C	
Storage temperature	-20~+60	°C	
Module size	Refer to outline drawing	mm	
Active Area(W×H)	222.72X125.28	mm	
Number of Dots	1024×600	dots	
Controller	HX8282A14 & HX8696	-	
Power Supply Voltage	3.3	V	
Outline Dimensions	Refer to outline drawing	-	
Backlight	3X13-LEDs (white)	pcs	
Weight	---	g	
Interface	RGB888	-	

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4.Outline.Drawing



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5. Absolute Maximum Ratings($T_a=25^\circ\text{C}$)

5.1 Electrical Absolute Maximum Ratings.($V_{SS}=0\text{V}$, $T_a=25^\circ\text{C}$)

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V_{DVDD}	-0.3	5.0	V	1, 2
	AV_{DD}	6.5	13.5	V	
	V_{GH}	-0.3	42.0	V	
	V_{GL}	-20	0.3	V	
	$V_{GH}-V_{GL}$	-	40.0	V	

Notes:

1. If the module is above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.
2. $V_{DVDD} > V_{SS}$ must be maintained.
3. Please be sure users are grounded when handing LCD Module.

5.2 Typical operation conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	DVDD	3.0	3.3	3.6	V	
	AVDD	10.2	10.5	10.8	V	
	VGH	20	21	22	V	
	VGL	-5.0	-5.5	-6.0	V	
Input signal voltage	VCOM	3.3	3.7	4.2	V	
Input logic high voltage	V_{IH}	0.7 DVDD	-	DVDD	V	
Input logic low voltage	V_{IL}	0	-	0.3DVDD	V	

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5.3 Environmental Absolute Maximum Ratings.

Item	Storage		Operating		Note
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	-20°C	60°C	-0°C	50°C	1,2
Humidity	-	-	-	-	3

1. The response time will become lower when operated at low temperature.

2. Background color changes slightly depending on ambient temperature.

The phenomenon is reversible.

3. $T_a \leq 40^\circ\text{C}$: 85%RH MAX.

$T_a \geq 40^\circ\text{C}$: Absolute humidity must be lower than the humidity of 85%RH at 40°C .

5.4 Parallel RGB input timing table

5.4.1 Resolution: 1024x600

- DE mode

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
DCLK Frequency	fclk	40.8	51.2	67.2	MHz
Horizontal Display Area	thd	1024			DCLK
HSD Period	th	1114	1344	1400	DCLK
HSD Blanking	thb+ thfp	90	320	376	DCLK
Vertical Display Area	tvd	600			T _H
VSD Period	tv	610	635	800	T _H
VSD Blanking	tvbp+ tvfp	10	35	200	T _H

DE mode (1024X600)

- HV mode

Horizontal timing

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
DCLK Frequency	fclk	44.9	51.2	63	MHz
Horizontal Display Area	thd	1024			DCLK
HSD Period	th	1200	1344	1400	DCLK
HSD Pulse Width	thpw	1	-	140	DCLK
HSD Back Porch	thbp	160			DCLK
HSD Front Porch	thfp	16	160	216	DCLK

HV mode horizontal timing (1024x600)

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Vertical Timing

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Vertical Display Area	tvd		600		T _H
VSD Period	tv	624	635	750	T _H
VSD Pulse Width	tvpw	1	-	20	T _H
VSD Back Porch	tvbp		23		T _H
VSD Front Porch	tvfp	1	12	127	T _H

HV Mode Vertical Timing (1024x600)

5.5 Timing

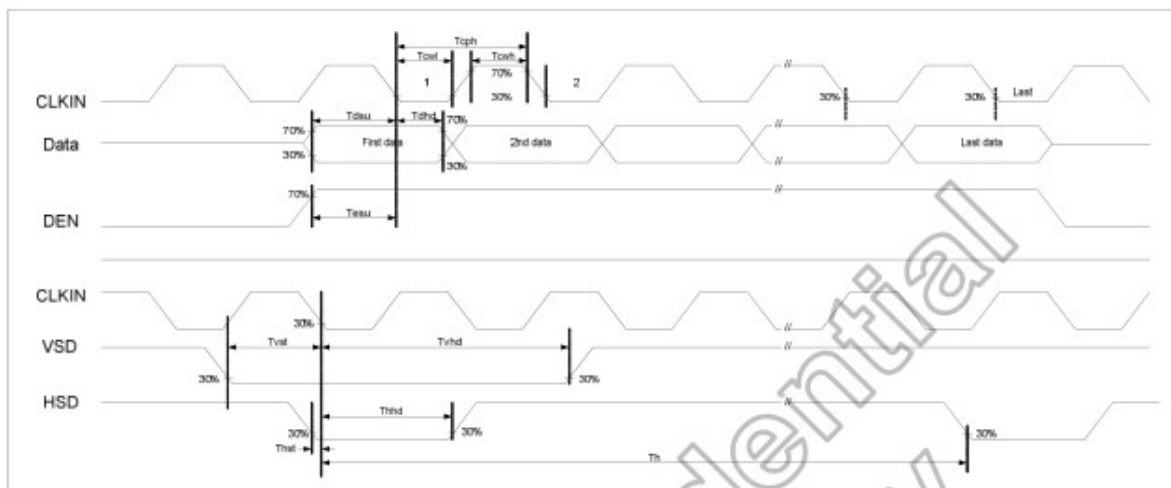
5.5.1 Output timing table

Parallel 24-bit RGB mode

Parameter	Symbol	Spec.			Unit	Conditions
		Min.	Typ.	Max.		
CLKIN Frequency	Fclk	-	65	71	MHz	VDD=3.0V~3.6V
CLKIN Cycle Time	Tclk	14.1	15.4	-	ns	-
CLKIN Pulse Duty	Tcwh	40	50	60	%	Tclk
Time from HSD to Source Output	Thso		64		CLKIN	-
Time from HSD to LD	Thld		64		CLKIN	-
Time from HSD to STV	Thstv		2		CLKIN	-
Time from HSD to CKV	Thckv		20		CLKIN	-
Time from HSD to OEV	Thoev		4		CLKIN	-
LD Pulse Width	Twld		10		CLKIN	-
CKV Pulse Width	Twckv		66		CLKIN	-
OEV Pulse Width	Twoev		74		CLKIN	-

Parallel 24-bit RGB mode

5.5.2 Timing diagram



Input clock and data timing diagram

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6. Electrical Specifications and Instruction Code

6.1 Electrical characteristics($V_{SS}=0V, T_a=25^\circ C$)

Parameter	Symbol	Condition	Min	Typ	Max	Unit	Note	
Power supply	DVDD	$T_a=25^\circ C$	3.0	3.3	3.6	V		
Input voltage	'H'	V_{IH}	$V_{DVDD}=3.3V$	$0.8V_{DVDD}$	-	V_{DVDD}	V	
	'L'	V_{IL}	$V_{DVDD}=3.3V$	0	-	$0.2V_{DVDD}$	V	
Current Consumption	I_{DVDD1}	Normal mode	-	25	50	mA	1	
	I_{DVDD2}	Sleep mode	-	0.05	0.1	mA	1	
Clock Frequency	f_{CLK}	-	-	30	50	MHz		

Note: 1: Tested in 1×1 chessboard pattern.

6.2 LED backlight specification($V_{SS}=0V, T_a=25^\circ C$)

Item	Symbol	Condition	Min	Typ	Max	Unit	Note
Supply voltage	V_f	$I_f=13 \times 20mA$	-	9	-	V	
Uniformity	ΔB_p	$I_f=13 \times 20mA$	75	-	-	%	

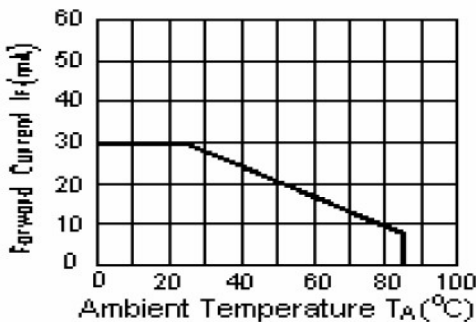
Note:

1: $V_{LED} = V_{LED(+)} - V_{LED(-)}$.

2: The current of LED is 260mA.

A LED drive in constant current mode is recommended.

3: LED power consumption is around 0.132W.



ILED VS TEMP

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6.3 Interface signals

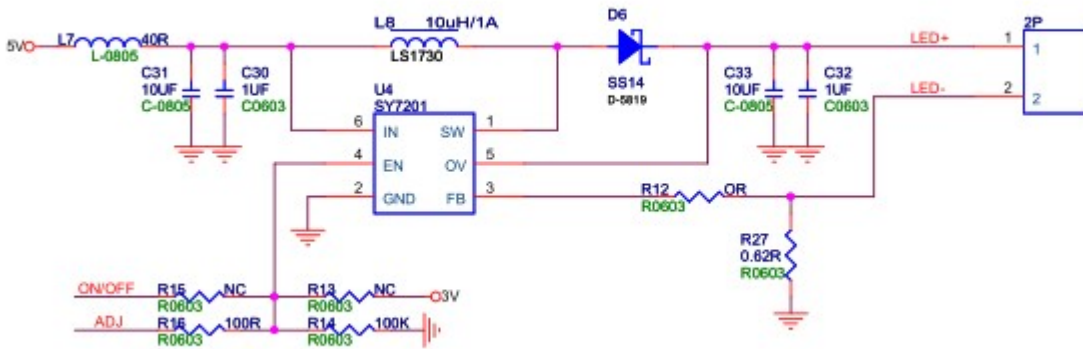
Pin No.	Symbol	I/O	Function
1-4	NC	-	No connection.
5	GND	P	Ground.
6	VCOM	I	Common voltage input
7	DVDD	P	Power for digital Circuit
8	MODE	I	Nomal diplay and standby mode select pin
9	DE	I	Data Enable
10	VS	I	Frame sync signal
11	HS	I	Line signal input
12-19	B7-B0	I	Blue data inpu
20-27	G7-G0	I	Green data inpu
28-35	R7-R0	I	Red data inpu
36	GND	P	Power ground
37	DCLK	I	Sample clock
38	GND	P	Power ground
39	L/R	I	Select left to right scanning direction
40	U/D	I	Select up or down scanning direction
41	VGH	I	Positive power for scan driver
42	VGL	I	Negative power for scan driver
43	AVDD	P	Power for analog circuit
44	RESET	I	Reset the display
45	NC	-	No Connection
46	VCOM	I	Common voltage input
47	DITHB	I	Dithering function enable control.
48	GND	P	Ground.
49-50	NC	-	No connection.

NOTE:

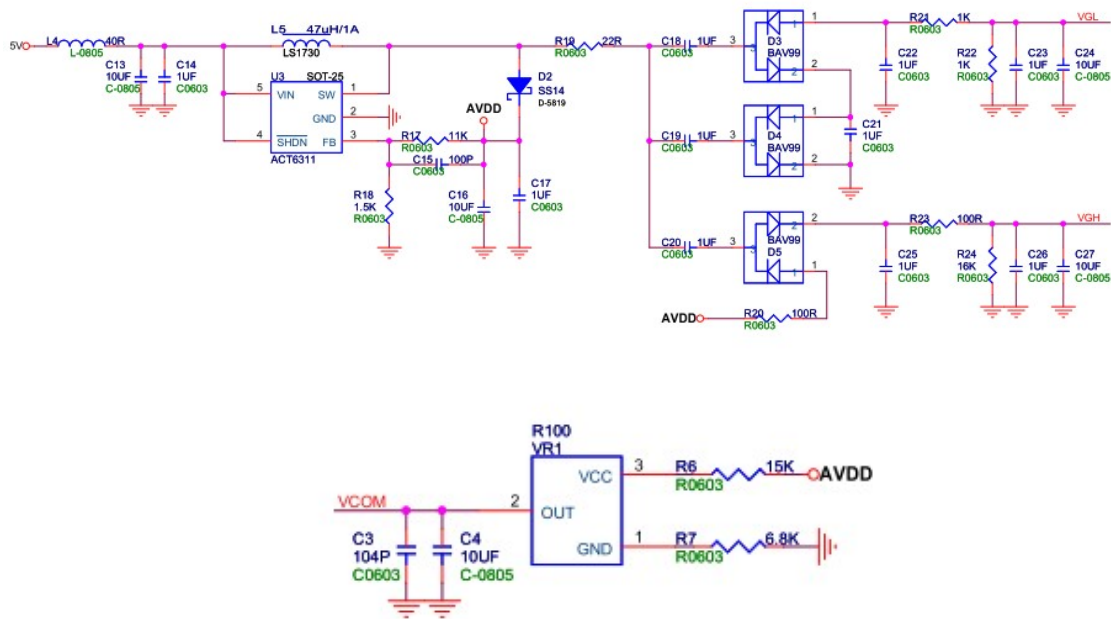
Setting of scan control input		Scanning direction
U/D	L/R	
DV _{DD}	DV _{DD}	Up to down, left to right
GND	DV _{DD}	Down to up, left to right
DV _{DD}	GND	Up to down, right to left
GND	GND	Down to up, right to left

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6.4 Backlight Driver Reference Circuit



6.5 DC/DC Reference Circuit



7. Optical Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Brightness	Bp	$\theta=0^\circ$	-	380	-	Cd/m ²	1
Uniformity	ΔBp	$\Phi=0^\circ$	75	-	-	%	1,2
Viewing Angle	3:00	$Cr \geq 10$	-	65	-	Deg	3
	6:00		-	55	-		

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	9:00			-	65	-		
	12:00			-	65	-		
Contrast Ratio	Cr		$\theta=0^\circ$ $\Phi=0^\circ$	300	500	-	-	4
Response Time	T _r			-	10		ms	5
	T _f			-	10		ms	
Color of CIE Coordinate	W	x		$\theta=0^\circ$ $\Phi=0^\circ$		0.28		-
		y			0.33		-	
	R	x			0.51		-	
		y			0.34		-	
	G	x			0.31		-	
		y			0.56		-	
	B	x			0.15		-	
		y			0.14		-	
NTSC Ratio	S		50	60	-	%		

Note : The parameter is slightly changed by temperature, driving voltage and materiel

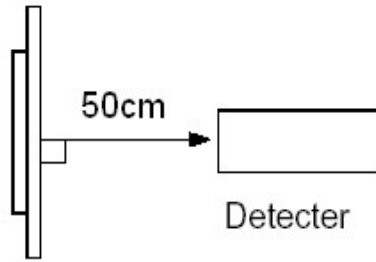
Note 1: The data are measured after LEDs are turned on for 5 minutes. LCM displays full white. The brightness is the average value of 9 measured spots. Measurement equipment BM-7 (Φ5mm)

Measuring condition:

- *Measuring surroundings: Dark room.*
- *Measuring temperature: Ta=25 °C.*
- *Adjust operating voltage to get optimum contrast at the center of the display.*

Measured value at the center point of LCD panel after more than 5 minutes while backlight turning on.

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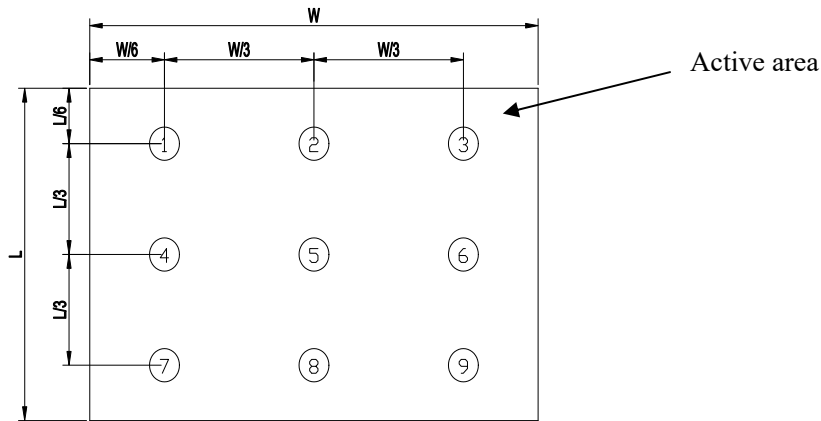


Note 2: The luminance uniformity is calculated by using following formula.

$$\Delta Bp = Bp (\text{Min.}) / Bp (\text{Max.}) \times 100 (\%)$$

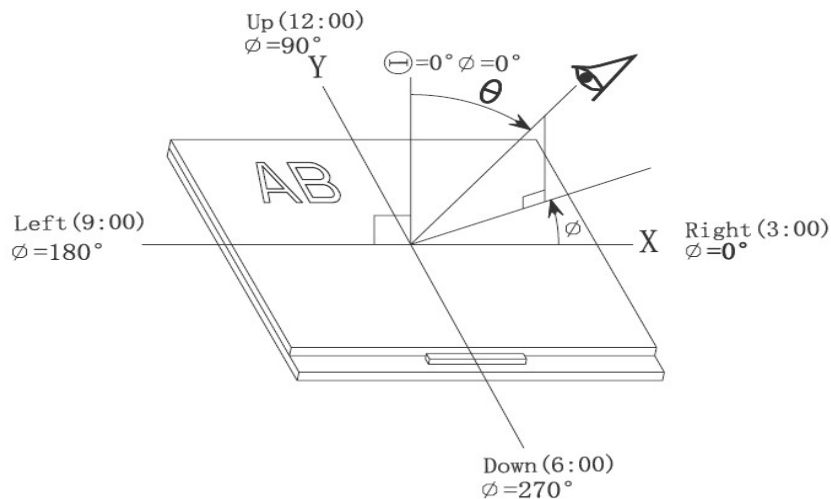
$Bp (\text{Max.})$ = Maximum brightness in 9 measured spots

$Bp (\text{Min.})$ = Minimum brightness in 9 measured spots.



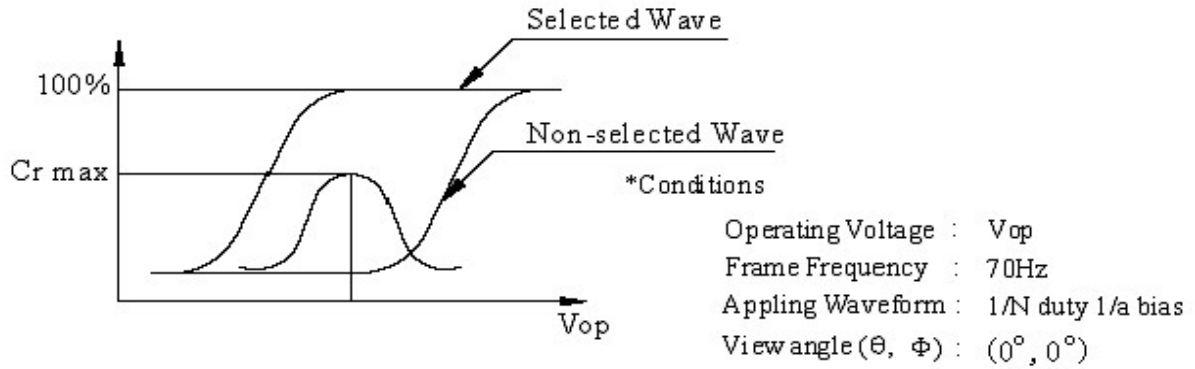
Note 3: The definition of viewing angle:

Refer to the graph below marked by ϑ and ϕ



Note 4: Definition of contrast ratio.(Test LCD using DMS501)

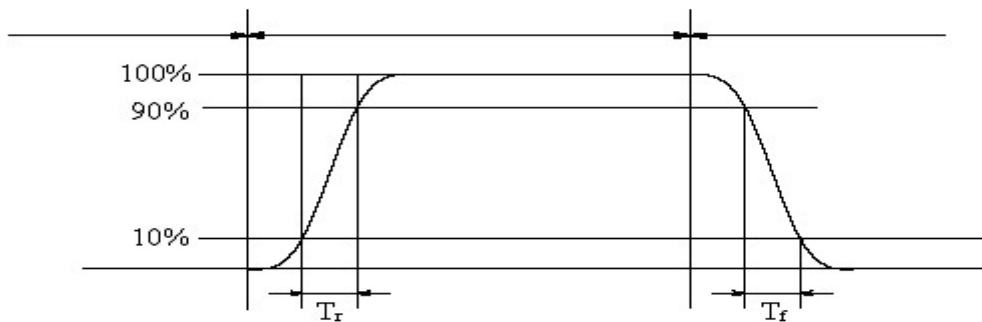
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$$\text{Contrast ratio}(Cr) = \frac{\text{Brightness of selected dots}}{\text{Brightness of non-selected dots}}$$

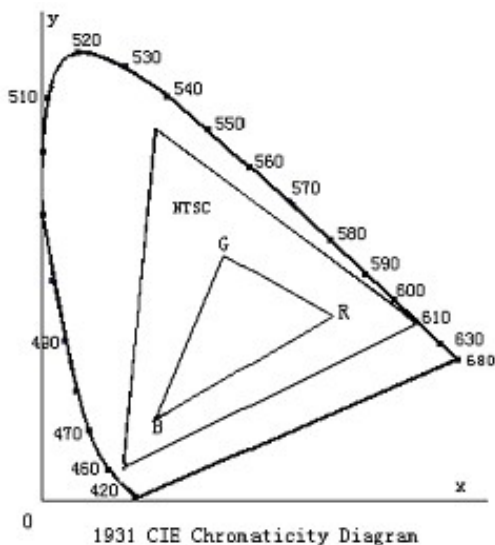
Note 5: Definition of Response time. (Test LCD using DMS501):

The output signals of photo detector are measured when the input signals are changed from "black" to "white"(falling time) and from "white" to "black"(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



The definition of response time

Note 6: Definition of Color of CIE Coordinate and NTSC Ratio.



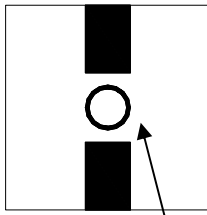
Color gamut:

$$S = \frac{\text{area of RGB triangle}}{\text{area of NTSC triangle}} \times 100\%$$

Note 7: Definition of cross talk.

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*Cross talk ratio(%)=|pattern A Brightness-pattern B Brightness|/pattern A Brightness*100*



Pattern A



Pattern B

Measurement point(center)

Electric volume value=3F+/-3Hex

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8. Reliability Test Items and Criteria

No	Test Item	Test condition	Criterion
1	High Temperature Storage	60°C±2°C 96H Restore 2H at 25°C Power off	1. After testing, cosmetic and electrical defects should not happen. 2. Total current consumption should not be more than twice of initial value.
2	Low Temperature Storage	-20°C±2°C 96H Restore 2H at 25°C Power off	
3	High Temperature Operation	50°C±2°C 96H Restore 2H at 25°C Power on	
4	Low Temperature Operation	-0°C±2°C 96H Restore 4H at 25°C Power on	
5	High Temperature/Humidity Operation	60°C±2°C 90%RH 96H Power on	
6	Temperature Cycle	-20°C → 60°C 30min 5min 30min after 5 cycle, Restore 2H at 25°C Power off	
7	Vibration Test	10Hz~150Hz, 100m/s ² , 120min	Not allowed cosmetic and electrical defects.
8	Shock Test	Half- sine wave, 300m/s ² , 11ms	
9	ESD TEST	Air: ± 8KV 150pF/330 Ω 5 times	
		Contact: ± 4KV 150pF/330 Ω 5 times	

Note: Operation: Supply 3.3V for logic system.

The inspection terms after reliability test, as below

ITEM	Inspection
Contrast	CR>50%
IDD	IDD<200%
Brightness	Brightness>60%
Color Tone	Color Tone+/-0,05

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9. Precautions for Use of LCD Modules

9.1 Handling Precautions

9.1.1 *The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.*

9.1.2 *If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.*

9.1.3 *Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.*

9.1.4 *The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.*

9.1.5 *If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:*

— Isopropyl alcohol — Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

— Water — Ketone — Aromatic solvents

9.1.6 *Do not attempt to disassemble the LCD Module.*

9.1.7 *If the logic circuit power is off, do not apply the input signals.*

9.1.8 *To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.*

a. Be sure to ground the body when handling the LCD Modules.

b. Tools required for assembly, such as soldering irons, must be properly ground.

c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

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9.2 Storage precautions

9.2.1 *When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.*

9.2.2 *The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:*

Temperature : 0°C ~ 40°C

Relatively humidity: ≤80%

9.2.3 *The LCD modules should be stored in the room without acid, alkali and harmful gas.*

9.3 *The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.*

END
