IIDWay Technology Co. Ltd

PRODUCT SPECIFICATIONS

For Customer:_____ ☐ : APPROVAL FOR SPECIFICATION

Customer Model No._____ ☐ : APPROVAL FOR SAMPLE

Module No.: *GZ70101-PIT043TWV* *Date : 2024.5.27*

Table of Contents

For Customer's Acceptance:

Approved By	Comment

PREPARED	CHECKED	APPROVER
DJF		

2. Revision Record

Date	Rev.No.	Page	Revision Items	Prepared
2024.5.27	V0		The first release	DJF

3. General Specifications

GZ70101-PIT043TWV is a TFT-LCD module. It is composed of a TFT-LCD panel, driver IC, FPC,a back light and CTP unit . The 4.3″ display area contains 800x480 pixels and can display up to 16.7M colors. This product accords with RoHS environmental criterion.

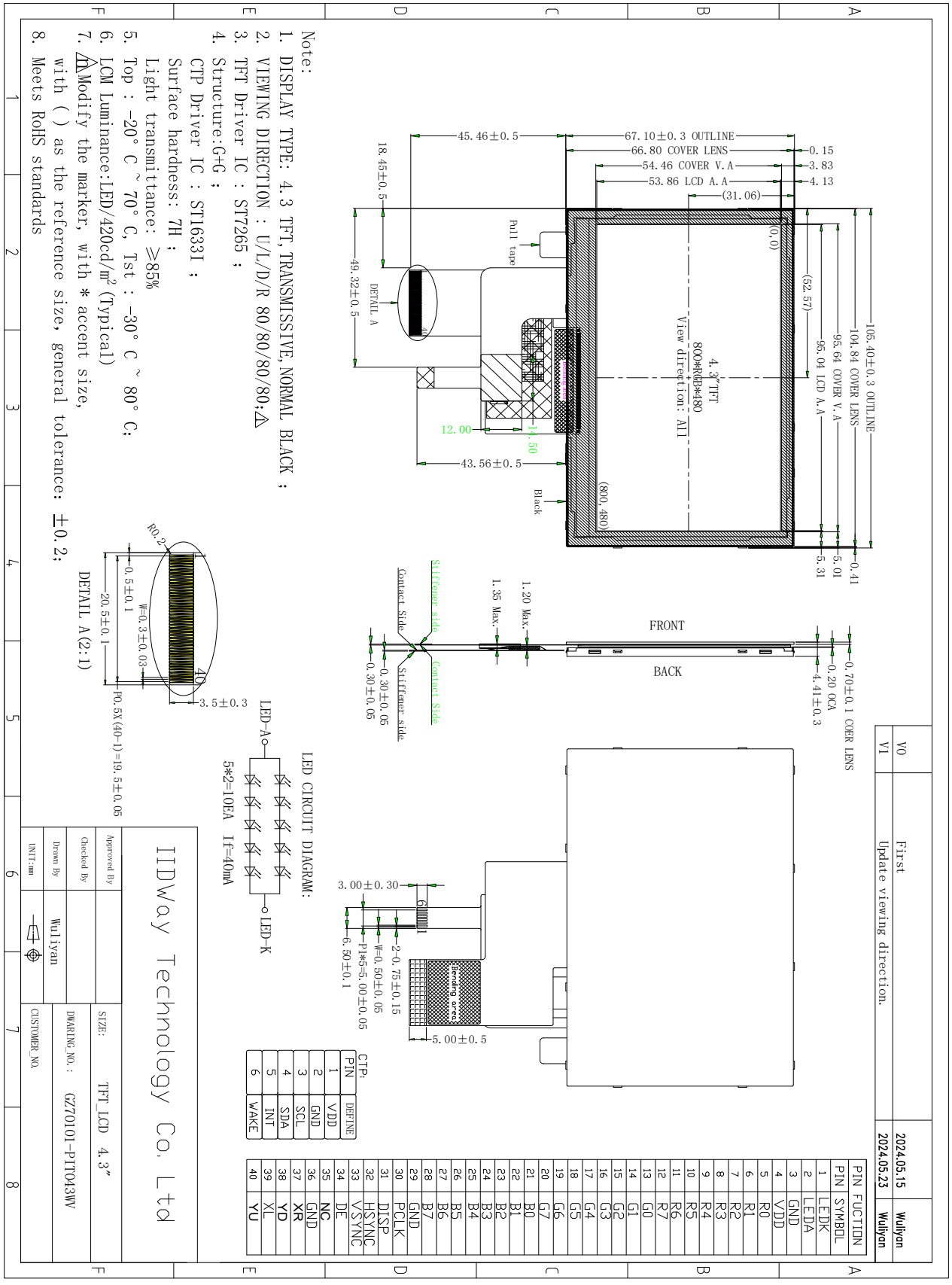
3.1 LCD Parameter

Item	Contents	Unit	Note
LCD Type	TFT	-	
Display color	16.7M		
Viewing Direction	ALL	O'Clock	
Operating temperature	-20~+70	℃	
Storage temperature	-30~+80	℃	
Module size	Refer to outline drawing	mm	
Active Area(W×H)	95.04X53.86	mm	
Number of Dots	*800x480*	dots	
Driver IC	ST7265	-	
Power Supply Voltage	3.3	V	
Backlight	5S2P-LEDs	pcs	
Interface	RGB	-	

3.2 CTP Parameter

Item	Contents	Unit	Note
Outline Size	Refer to outline drawing	mm	
Cover View Area	*95.64(H)X54.46(V)*		
CTP Resolution	*800x480*	dots	
Interface Mode	*IIC*	-	
Touch Mode	*5 Human fingers multi-touch*	-	
Surface hardness	*>=6H*	-	
Transparency	*>=86%*	-	
Accuracy	*Entre +/-1.5mm,Edge +/-2.5mm*	mm	
CTP Controller	*ST1633I*	-	
Power Supply Voltage	*3.3*	V	

4.Outline.Drawing



5. Absolute Maximum Ratings(Ta=25℃)

5.1 Electrical Absolute Maximum Ratings.(Vss=0V ,Ta=25℃)

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	VDD	-0.3	4.0	V	1，2
CTP Power Supply Voltage	VDD	2.8	3.3	V	1，2

Notes:

1. If the module is above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.

2. V_{DD} >V_{SS} must be maintained.

3. Please be sure users are grounded when handing LCD Module.

5.2 Environmental Absolute Maximum Ratings.

Item	Storage		Operating	
	MIN.	MAX.	MIN.	MAX.
Ambient Temperature	-30℃	80℃	-20℃	70℃
Humidity	-	-	-	-

1. The response time will become lower when operated at low temperature.

2. Background color changes slightly depending on ambient temperature.

The phenomenon is reversible.

3. Ta<=40 ℃:90%RH MAX.

Ta>=40 ℃:Absolute humidity must be lower than the humidity of 90%RH at 40 ℃.

6. Electrical Specifications and Instruction Code

6.1 Electrical characteristics(Vss=0V ,Ta=25℃)

Parameter		Symbol	Condition	Min	Typ	Max	Unit	Note
Power supply		VDD	Ta=25℃	3.1	3.3	3.6	V	
Input voltage	'H'	V_{IH}	V_{DD}=3.3V	0.7V_{DD}	-	V_{DD}	V	
	'L'	V_{IL}	V_{DD}=3.3V	0	-	0.3V_{DD}	V	

Note: If one of the above items is exceeded its maximum limitation momentarily, the quality of the product may be degraded. Absolute maximum limitation, therefore, specify the values exceeding which the product may be physically damaged. Be sure to use the product within the recommend range.

6.2 LED backlight specification(VSS=0V ,Ta=25℃)

Item	Symbol	Condition	Min	Typ	Max	Unit	Note
Supply voltage VLED	V_f	If=40mA	13.5	15	16.5	V	
Uniformity	∆Bp	If=40mA	80	-	-	%	
LED Life Time	-	-	30K	-	-	hr	1

Note 1: Brightness to be decreased to 50% of the initial value at ambient temperature TA=25℃

6.3 Interface signals

Pin No.	Symbol	I/O	Function
1	LEDK	P	LED back light(Cathode)
2	LEDA	P	LED back light(Anode)
3	GND	P	Ground
4	VDD	P	Power supply
5-12	R0-R7	I	Red data bus
13-20	G0-G7	I	Green data bus
21-28	B0-B7	I	Blue data bus
29	GND	P	Ground
30	PCLK	I	clock signal
31	DISP	I	The display mode. DISP="0":Standby mode(Default) DISP="1":Normal display mode
32	HSYNC	I	Line synchronizing signal
33	VSYNC	I	Vertical synchronizing signal
34	DE	I	Data enable signal
35	NC		No connection
36	GND	P	Ground
37	XR	I	RTP control pin, no use please NC
38	YD	I	
39	XL	I	
40	YU	I	

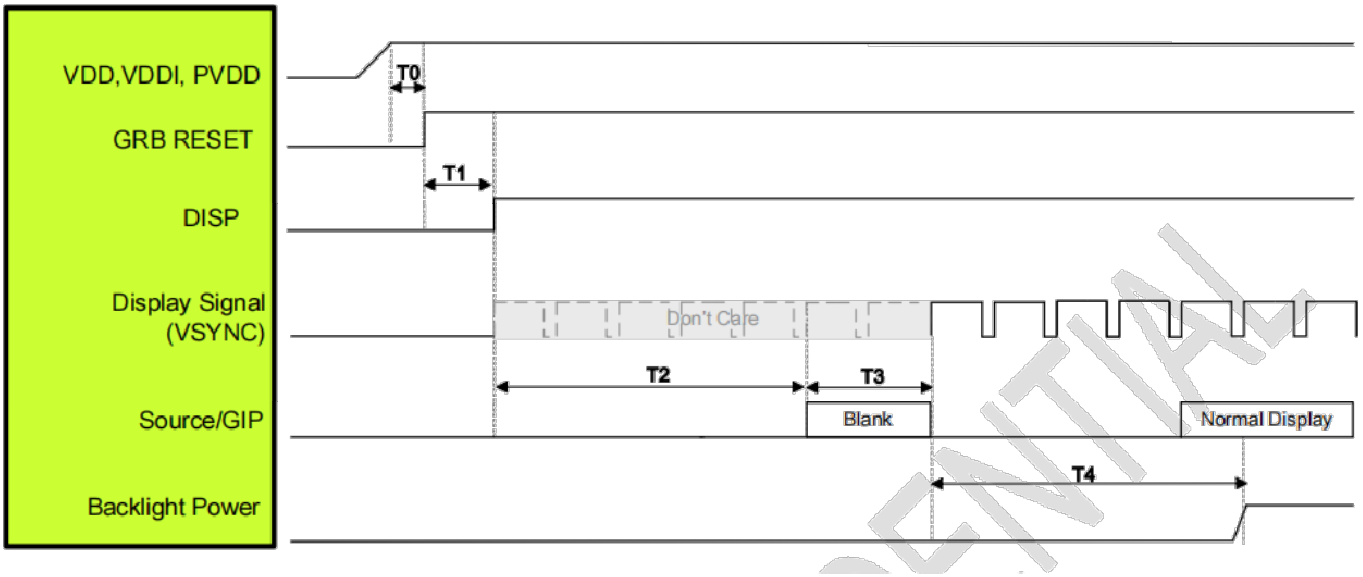
6.3.1 LCM PIN

CTP PIN

Pin No.	Symbol	I/O	Function
1	VDD	P	Power supply
2	GND	P	Ground
3	SCL	I	Serial interface clock
4	SDA	I/O	Serial input/output data bus
5	INT	O	External Interrupt pin
6	WAKE	I	Reset signal

6.4 Power Sequence

6.4.1 Power On Sequence



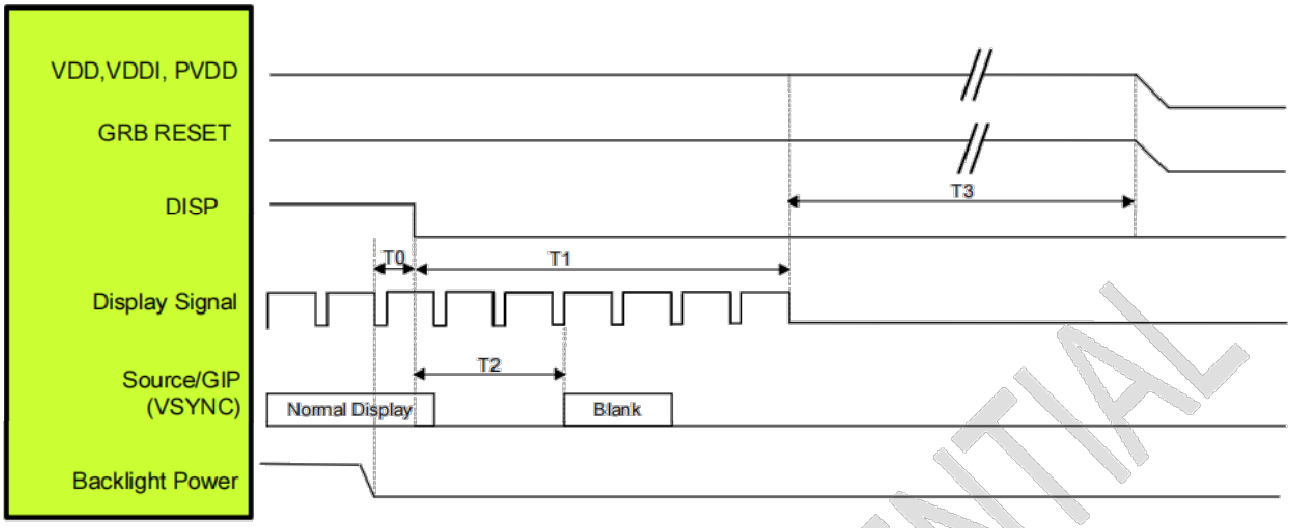
Symbol	Description	Time	Unit
T0	System power stability to GRB RESET signal	≥1	ms
T1	GRB RESET= "High" to DISP="High"	≥10	ms
T2	DISP="High" to Source/GIP scan blank	85	ms
T3	IC scan blanking signal	≥33	ms
T4	Display signal input to Backlight power on (base on Display Signal Frame Rate 60Hz)	≥100	ms

Note: 1. When DISP pull "H" or "L", IC will execute the internal power on or power off procedures .Please be careful about the timing of DISP and do not interrupt it during power on or power off procedure, otherwise unexpected errors will occur.

2. RGB interface Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0].

3. LVDS interface Display signal: DCLK P/N; RX[3:0] P/N.

6.4.2 Power OFF Sequence



Symbol	Description	Time	Unit
T0	Backlight Power off to DISP="Low"	≥1	ms
T1	DISP="Low" to IC internal voltage discharge complete	≥100	ms
T2	DISP="Low" to Source/GIP scan blank (base on Display Signal Frame Rate 60Hz)	≤50	ms
T3	IC internal voltage discharge is completed to VDD/VDDI/PVDD off	≥0	ms

Note: 1. When DISP pull "H" or "L", IC will execute the internal power on or power off procedures .Please be careful about the timing of DISP and do not interrupt it during power on or power off procedure, otherwise unexpected errors will occur.

2. RGB interface Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0].

3. LVDS interface Display signal: DCLK P/N; RX[3:0] P/N.

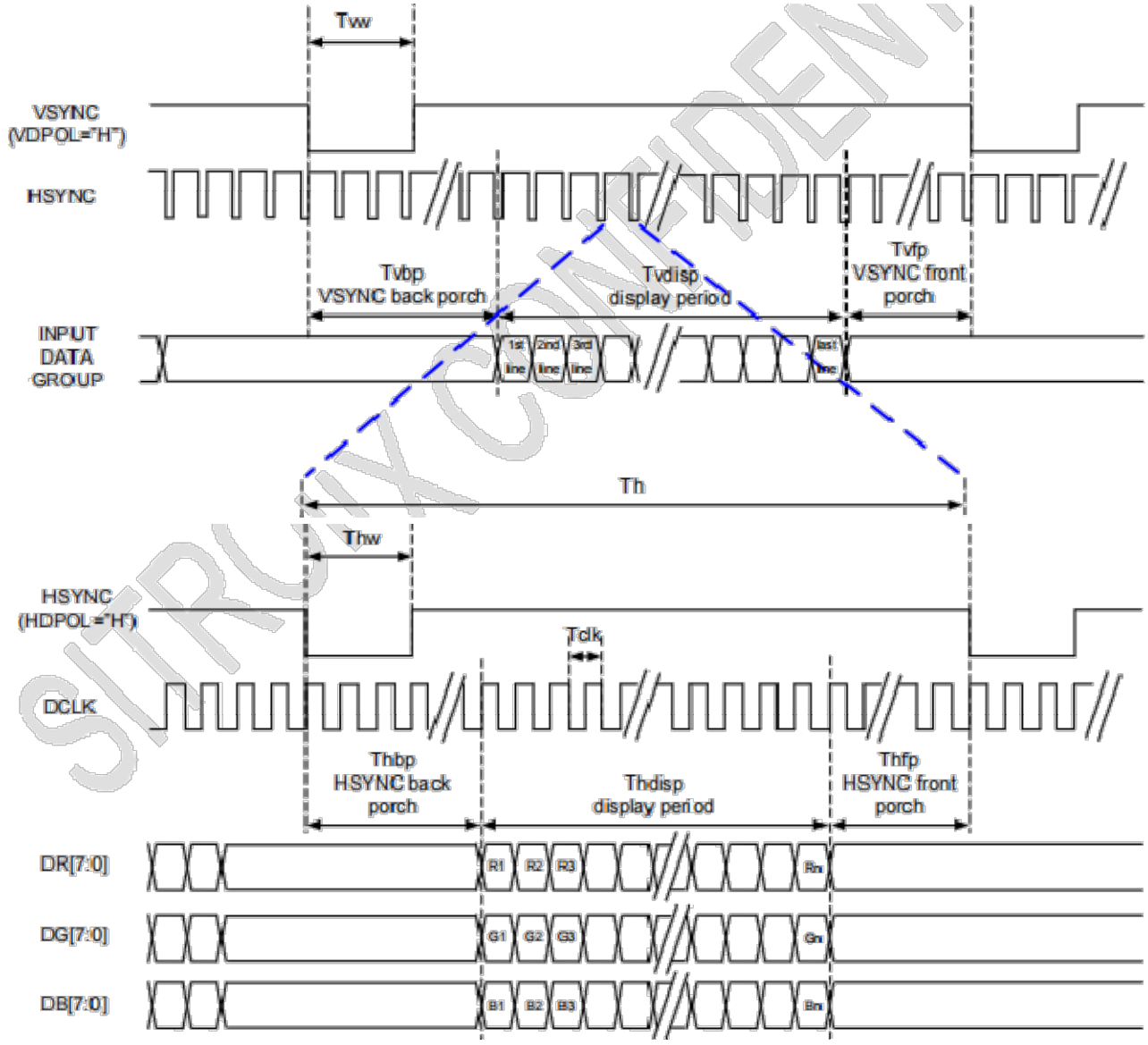
6.5 Timing Characteristics

6.5.1 RGB Interface

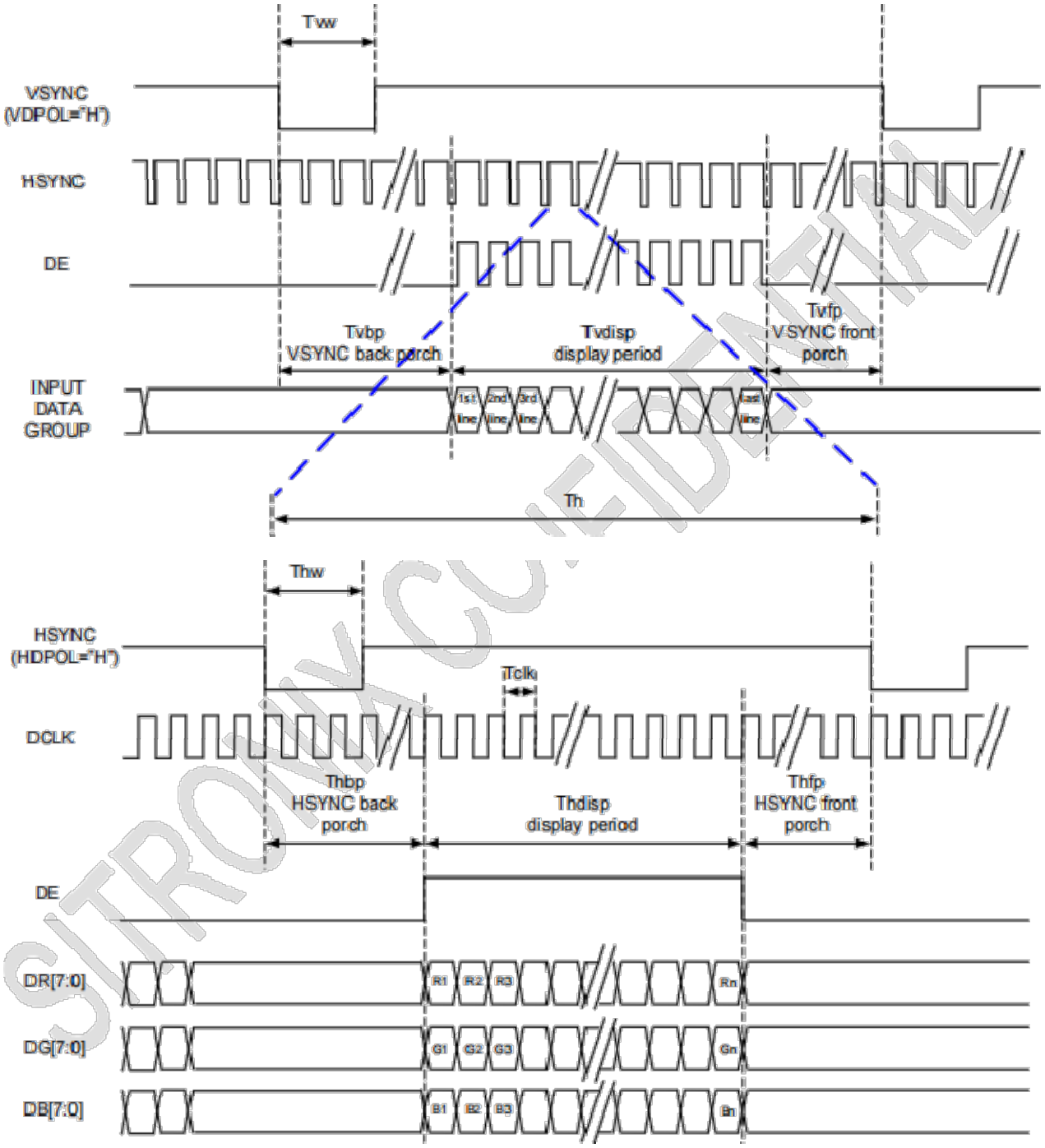
RGB Mode Selection Table	DCLK	HSYNC	VSYNC	DE
SYNC - DE Mode	Input	Input	Input	Input
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input

Note: "Input" means these signals are driven by host side.

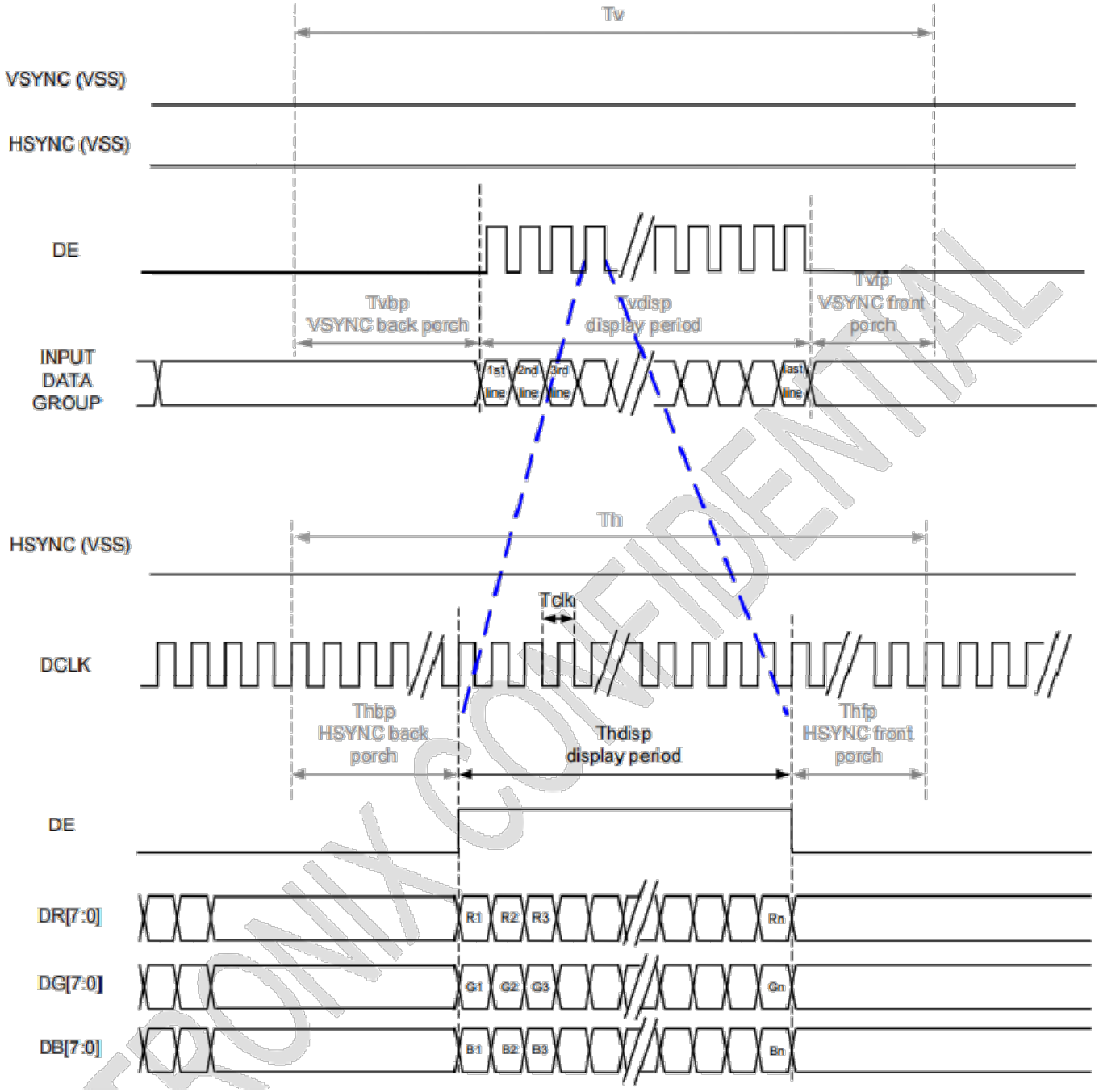
6.5.2 SYNC Mode



6.5.3 SYNC-DE Mode



6.5.4 DE Mode



7. Optical Characteristics

Item	Symbol		Condition	Min.	Typ.	Max.	Unit	Note
Brightness	Bp		θ=0° Φ=0°	-	420	-	cd/m^2	1
Uniformity	⊿Bp			80	-	-	%	1,2
Viewing Angle	3:00		Cr≥10	-	80	-	Deg	3
	6:00			-	80	-		
	9:00			-	80	-		
	12:00			-	80	-		
Contrast Ratio	Cr		θ=0° Φ=0°	-	1200	-	-	4
Response Time	T_r+T_f			-	30	-	ms	
Color of CIE Coordinate	W	x	θ=0° Φ=0°	TYP-0.05	TBD	TYP+0.05	-	1,6
		y					-	
	R	x					-	
		y					-	
	G	x					-	
		y					-	
	B	x					-	
		y					-	
NTSC Ratio	S			-	50	-	%	

Note：The parameter is slightly changed by temperature, driving voltage and materiel

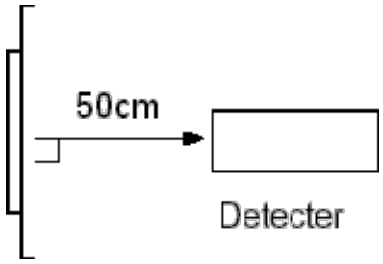
*Note 1: The data are measured after LEDs are turned on for 5 minutes. LCM displays full white. The brightness is the average value of 9 measured spots. Measurement equipm*ent BM-7 (Φ5mm)

Measuring condition:

- *Measuring surroundings: Dark room.*
- *Measuring temperature: Ta=25℃.*
- *Adjust operating voltage to get optimum contrast at the center of the display.*

Measured value at the center point of LCD panel after more than 5 minutes while backlight

turning on.

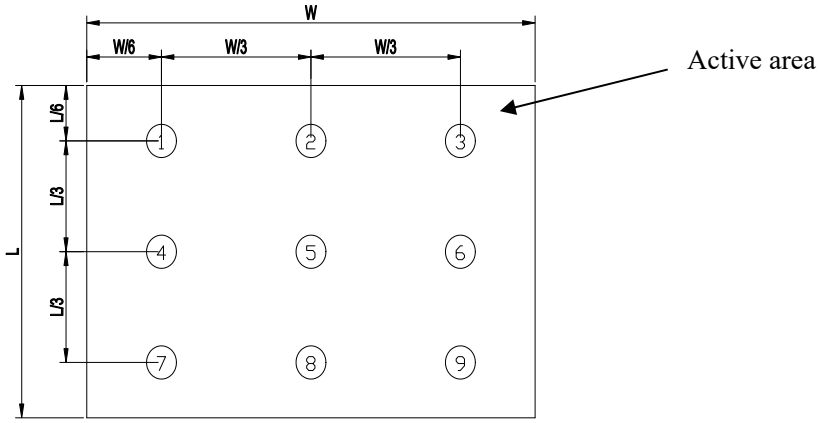


Note 2: The luminance uniformity is calculated by using following formula.

⊿Bp = Bp (Min.) / Bp (Max.)×100 (%)

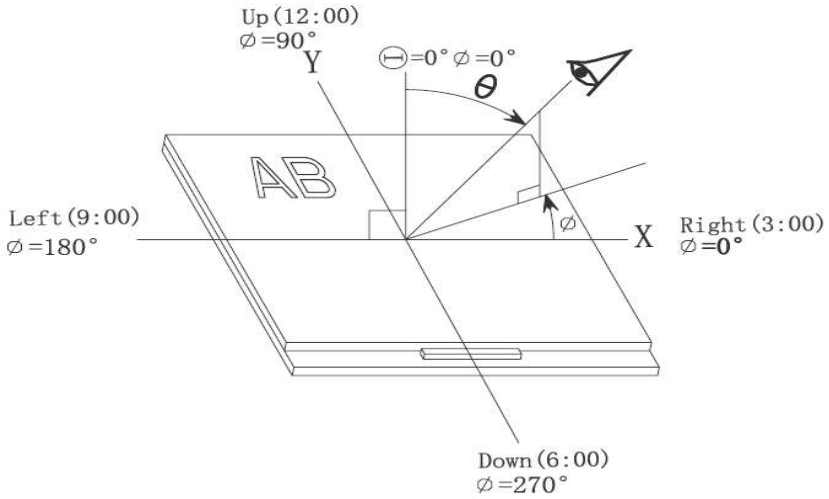
Bp (Max.) = Maximum brightness in 9 measured spots

Bp (Min.) = Minimum brightness in 9 measured spots.

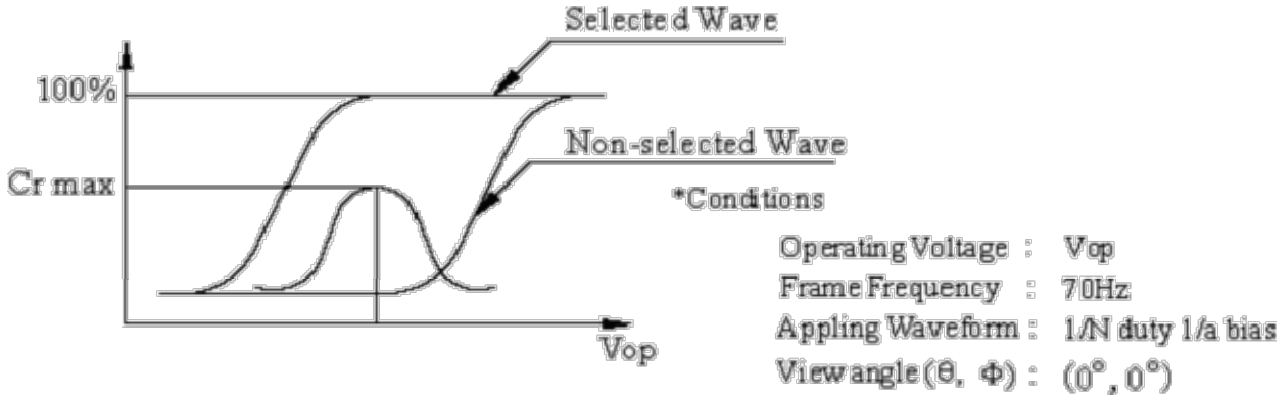


Note 3: The definition of viewing angle:

Refer to the graph below marked by ϑ and Φ



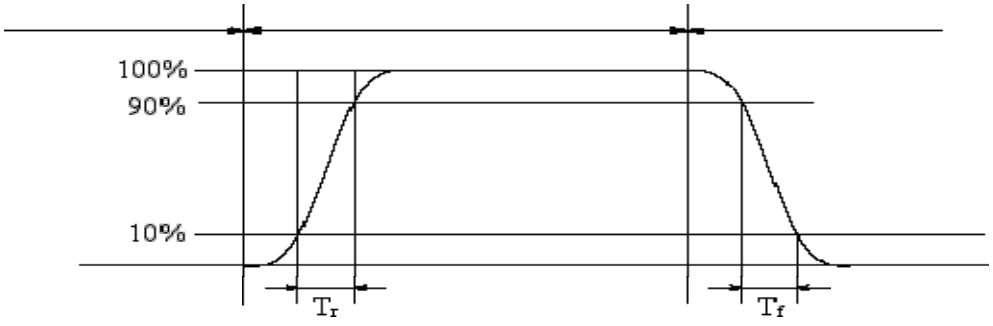
Note 4: Definition of contrast ratio.(Test LCD using DMS501)



$$Contrast\ ratio(Cr) = \frac{Brightness\ of\ selected\ dots}{Brightness\ of\ non-selected\ dots}$$

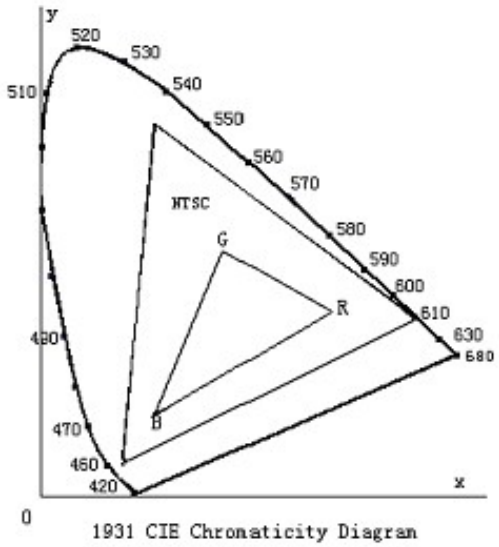
Note 5: Definition of Response time. (Test LCD using DMS501):

The output signals of photo detector are measured when the input signals are changed from "black" to "white"(falling time) and from "white" to "black"(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes.Refer to figure as below.



The definition of response time

Note 6: Definition of Color of CIE Coordinate and NTSC Ratio.

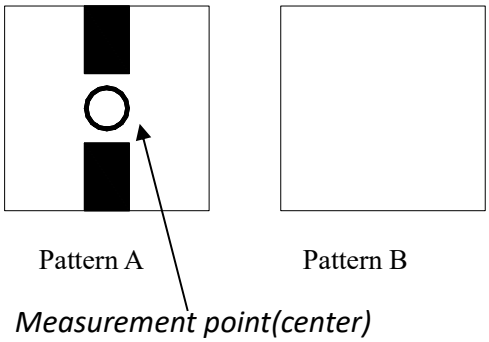


1931 CIE Chromaticity Diagram

Color gamut:

$$S = \frac{\text{area of RGB triangle}}{\text{area of NTSC triangle}} \times 100\%$$

Note 7: Definition of cross talk.
*Cross talk ratio(%)=|pattern A Brightness-pattern B Brightness|/pattern A Brightness*100*



Electric volume value=3F+/-3Hex

8. Reliability Test Items and Criteria

No	Test Item	Test condition	Criterion
1	High Temperature Storage	80℃±2℃ 96H Restore 2H at 25℃ Power off	1. After testing, cosmetic and electrical defects should not happen. 2. Total current consumption should not be more than twice of initial value.
2	Low Temperature Storage	-30℃±2℃ 96H Restore 2H at 25℃ Power off	
3	High Temperature Operation	70℃±2℃ 96H Restore 2H at 25℃ Power on	
4	Low Temperature Operation	-20℃±2℃ 96H Restore 4H at 25℃ Power on	
5	High Temperature/Humidity Operation	60℃±2℃ 90%RH 96H Power on	
6	Temperature Cycle	-30℃←——→80℃ 30min 5min 30min after 5 cycle, Restore 2H at 25℃ Power off	

Note: Operation: Supply 3.3V for logic system.

The inspection terms after reliability test, as below

ITEM	Inspection
Contrast	CR>50%
IDD	IDD<200%
Brightness	Brightness>60%
Color Tone	Color Tone+/-0,05

9. Precautions for Use of LCD Modules

9.1 Handling Precautions

9.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

9.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

9.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

9.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

9.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

— Isopropyl alcohol — Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

— Water — Ketone — Aromatic solvents

9.1.6 Do not attempt to disassemble the LCD Module.

9.1.7 If the logic circuit power is off, do not apply the input signals.

9.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

a. Be sure to ground the body when handling the LCD Modules.

b. Tools required for assembly, such as soldering irons, must be properly ground.

c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

9.2 Storage precautions

9.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

9.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0℃ ～ 40℃

Relatively humidity: ≤80%

9.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

9.3 ***The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.***

END